

Chhattisgarh Swami Vivekanand Technical University, Bhilai

Scheme of teaching and examination

M.Tech. (DIGITAL ELECTRONICS) in the Department of Electronics & Telecommunication

1st SEMESTER

S.No	Board of Study	Subject Code	Subject Name	Periods per week			Scheme of Exam			Total Marks	Credit L+(T+P)/2
				L	T	P	Theory/Practical				
							ESE	CT	TA		
1	Electronics & Telecom.	555111 (28)	Advance Digital Signal Processing	3	1	-	100	20	20	140	4
2	Electronics & Telecom.	555112 (28)	Advanced Microprocessors & Interfaces	3	1	-	100	20	20	140	4
3	Electronics & Telecom.	555113 (28)	Digital Communication Techniques	3	1	-	100	20	20	140	4
4	Electronics & Telecom.	555114 (28)	VLSI Design	3	1	-	100	20	20	140	4
5	Refer Table – I		ELECTIVE –I		1	-	100	20	20	140	4
6	Electronics & Telecom.	555121 (28)	Advanced Digital Signal Processing Lab	--	-	3	75	--	75	150	2
7	Electronics & Telecom.	555122 (28)	Advanced Microprocessors & Interfaces Lab	-	-	3	75	--	75	150	2
TOTAL				15	5	6	650	100	250	1000	24

L-Lecture, T- Tutorial, P- Practical, ESE- End Semester Examination, CT- Class Test,TA- Teacher's Assessment

Note : Duration of all theory papers will be of Three Hours.

Table – I		
Elective – I		
Board of Study	Code	Subject
Electronics & Telecom.	555131 (28)	Fiber Optic Networks
Electronics & Telecom.	555132 (28)	Reliability Engineering
Electronics & Telecom.	555133 (28)	Parallel Processors
Electronics & Telecom.	555134 (28)	Digital Instrumentation

Note (1) - 1/4th of total strength of students subject to minimum of twenty students is required to offer an elective in the college in a Particular academic session .

Note (2) - Choice of elective course once made for an examination cannot be changed in future examinations.



(Signature)
PRINCIPAL

Rungta College of Engg. & Tech.
Kohka Kurud Road, Kurud, Bhilai-490024

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II Semester

S.No	Board of Study	Subject Code	Subject Name	Periods per week			Scheme of Exam			Total Marks	Credit L+(T+P)/2
				L	T	P	Theory/Practical				
							ESE	CT	TA		
1	Electronics & Telecom.	555211 (28)	Digital System Design using VHDL	3	1	-	100	20	20	140	4
2	Electronics & Telecom.	555212 (28)	Electronics System Design	3	1	-	100	20	20	140	4
3	Electronics & Telecom.	555213 (28)	Microcontroller & Embedded System Design	3	1	-	100	20	20	140	4
4	Electronics & Telecom.	555214 (28)	Computer Communication Network	3	1	-	100	20	20	140	4
5	Refer Table - II		Elective –II	3	1	-	100	20	20	140	4
6	Electronics & Telecom.	555221 (28)	Digital System Design using VHDL Lab	--	-	3	75	--	75	150	2
7	Electronics & Telecom.	555222 (28)	Electronics System Design Lab	-	-	3	75	--	75	150	2
Total				15	5	6	650	100	250	1000	24


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 Note : Duration of all theory papers will be of Three Hours.

Table – II		
Elective – II		
Board of Study	Code	Subject
Electronics & Telecom.	555231 (28)	Artificial Intelligence & Expert System
Electronics & Telecom.	555232 (28)	Digital Satellite Communication
Electronics & Telecom.	555233 (28)	Cellular Network Design
Electronics & Telecom.	555234 (28)	Digital Image Processing

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III SEMESTER

S.No	Board of Study	Subject Code	Subject Name	Periods per week			Scheme of Exam			Total Marks	Credit L+(T+P)/2
				L	T	P	Theory/Practical				
							ESE	CT	TA		
1	Electronics & Telecom	555311 (28)	Digital Coding Techniques	3	1	-	100	20	20	140	4
2	Refer Table – III		Elective –III	3	1	-	100	20	20	140	4
3	Electronics & Telecom	555321 (28)	Preliminary Project	-	-	15	100	-	100	200	8
4	Electronics & Telecom	555322 (28)	Seminar based on Dissertation	-	-	3	-	-	20	20	2
Total				6	2	18	300	40	160	500	18


L-Lecture, T- Tutorial, P- Practical, ESE- End Semester Examination, CT- Class Test, TA- Teacher's Assessment

Table – III		
Elective – III		
Board of Study	Code	Subject
Electronics & Telecom	555331 (28)	Network Modelling
Electronics & Telecom	555332 (28)	Neural Network and Application
Electronics & Telecom	555333 (28)	ASIC Design
Electronics & Telecom	555334 (28)	Digital Switching Systems

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IV SEMESTER


S.No	Board of Study	Subject Code	Subject Name	Periods per week			Scheme of Exam			Total Marks	Credit L+(T+P)/2
				L	T	P	Theory/Practical				
							ESE	CT	TA		
1	Electronic & Telecom..	555421 (28)	PROJECT + Seminar	6	-	34	300	-	200	500	23
Total				6	-	34	300	-	200	500	23

L-Lecture, T- Tutorial, P- Practical, ESE- End Semester Examination, CT- Class Test,TA- Teacher's Assessment

Scheme of marks Allotment

Semester	Total Marks	Grand Total
I	1000	3000
II	1000	
III	500	
IV	500	




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